7/15/3

SHEET 1 OF 1

Form PTO 1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 240324US-2 TTC DIV	SERIAL NO. 10/618,614 ~~NEW APPLICATION~~
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Tohru OZAKI, et al.	
		FILING DATE HEREWITH	GROUP Unassigned 2815

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	5,281,555	01/1994	CHO	438	625	
	AB	5,361,234	11/1994	IWASA	365	210	
	AC	5,410,161	04/1995	NARITA	257	41	
	AD	5,689,126	11/1997	TAKAISHI	257	306	
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	TRANSLATION NO
	AO	4-05-129552	05-1993	JAPAN		
	AP	4-08-181290	07-1996	JAPAN		
	AQ	4-11-354727	12-1999	JAPAN		
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	Takashima et al., A Sub-40ns Random-Access Chain FRAM Architecture with a 7ns Cell-Plate-Line Drive, 1999, IEEE, International Solid State Circuits Conference, pp. 102, 103, 150
	AX	Takeshima et al., Gain Cell Block Architecture for Gigabit-Scale Chain Ferroelctric RAM, June 1999, Symposium on VLSI Circuits Digest of Technical Papers, pp. 103-04
	AY	
	AZ	☐ Additional References sheet(s) attached

Examiner [signature] Date Considered 9/1/04

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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